

CLAIMS:

1. A liquid crystal display apparatus, comprising:

a first substrate;

a second substrate arranged opposite the first substrate;

a liquid crystal layer sandwiched between said first substrate and said second substrate; and

a plurality of pixels which are sandwiched between said first substrate and said second substrate and form a display section,

wherein each of said pixels is provided with a first pixel electrode and a second pixel electrode both corresponding to said pixel, and a common electrode corresponding to said first and second pixel electrodes.

2. The liquid crystal display apparatus according to claim 1, wherein said first and second pixel electrodes can provide each of said pixels with a corresponding potential.

3. The liquid crystal display apparatus according to claim 1, further comprising:

a first signal driver for supplying a potential to said first pixel electrodes;

a second signal driver for supplying a potential to said second pixel electrodes; and

a signal control circuit for controlling signals transmitted to said first and second signal

drivers.

4. The liquid crystal display apparatus according to claim 1, wherein said first pixel electrode, said common electrodes, and said second pixel electrodes are disposed on said first substrate.

5. The liquid crystal display apparatus according to claim 4,

wherein said first pixel electrode and said common electrode are linear and are arranged substantially in parallel, and

said second pixel electrode is located between said first pixel electrode and said common electrode.

6. The liquid crystal display apparatus according to claim 4,

wherein in each pixel, said first pixel electrode and said common electrode are linear and are arranged substantially in parallel, and

at least part of said second pixel electrode overlaps said first pixel electrode or said common electrode.

7. The liquid crystal display apparatus according to claim 6,

wherein said second pixel electrode is linear, and

said second pixel electrode is as wide as or narrower than the first pixel electrode or common electrode, which is overlapped by the part of the

DRAFTED BY DRAFTING SECTION

second pixel electrodes.

8. The liquid crystal display apparatus according to claim 6,

wherein said second pixel electrode is linear, and

said second pixel electrode is wider than said first pixel electrodes or common electrode, which is overlapped by the part of the second pixel electrode.

9. The liquid crystal display apparatus according to claim 4,

wherein said first pixel electrode and said common electrode are linear and are arranged substantially in parallel, and

said second pixel electrode is located below said first pixel electrode and said common electrode, said second pixel electrode overlaps said first pixel electrode and said common electrode, and insulated films are disposed between said second pixel electrode and said first pixel electrode and between said second pixel electrode and said common electrode.

10. The liquid crystal display apparatus according to claim 4,

wherein said first pixel electrode and said second pixel electrode are linear and are arranged substantially in parallel, and

said common electrode is located between said first pixel electrode and said second pixel electrode.

11. The liquid crystal display apparatus according to claim 4,

wherein said first and said second pixel electrodes are linear and are arranged substantially in parallel, and

at least part of said common electrode overlaps said first or said second pixel electrode.

12. The liquid crystal display apparatus according to claim 4,

wherein said first and said second pixel electrodes are linear and are arranged substantially in parallel, and

said common electrode is located below said first and second pixel electrodes, said common electrode overlaps said first and said second pixel electrodes, and insulated films are disposed between said common electrode and said first pixel electrode and between said common electrode and said second pixel electrode.

13. The liquid crystal display apparatus according to claim 5,

wherein plural pieces of said first pixel electrode arranged for said corresponding pixels are connected together via a first junction, plural pieces of said second pixel electrode arranged for said corresponding pixels are connected together via a second junction, and said plural pieces of said first pixel electrode and said first junction do not overlap

RECORDED SEARCHED SERIALIZED INDEXED

said plural pieces of second pixel electrodes and said second junction.

14. The liquid crystal display apparatus according to claim 13,

wherein said first pixel electrode, said second pixel electrode, said first signal line, and said second signal line are arranged in the same layer, and said common electrode and said scan line are arranged in the same layer.

15. The liquid crystal display apparatus according to claim 1,

wherein said first and said second pixel electrodes are disposed on said first substrate, and said common electrodes are disposed on said second substrate.

16. The liquid crystal display apparatus according to claim 15,

wherein said first and said second pixel electrodes are linear and are arranged substantially in parallel, and

said common electrode overlaps said first and said second pixel electrodes.

17. The liquid crystal display apparatus according to claim 15,

wherein a dielectric having a thickness of 1.5 μm or more is arranged on said common electrode.

18. The liquid crystal display apparatus according to claim 17,

wherein a portion of the dielectric which overlaps said common electrode includes a recess penetrating said dielectric or having a depth amounting to 50% or more of the thickness.

19. The liquid crystal display apparatus according to claim 5,

wherein when a difference between potentials provided to said first pixel electrode and to said common electrode is largest or smallest, a potential provided to said second pixel electrode is substantially equal to an average of the potentials provided to said first pixel electrode and to said common electrode.

20. The liquid crystal display apparatus according to claim 6,

wherein when the difference between the potentials provided to said first pixel electrode and to said common electrode is largest or smallest, the potential provided to said second pixel electrode is substantially equal to the potential provided to said first pixel electrode or common electrode, which is overlapped by said second pixel electrode.

21. The liquid crystal display apparatus according to claim 10,

wherein when the difference between the potentials provided to said first pixel electrode and to said common electrode is largest or smallest, the potential provided to said second pixel electrode is

substantially equal to the potential provided to said first pixel electrode.

22. The liquid crystal display apparatus according to claim 16,

wherein when a difference between the potentials provided to said first pixel electrode and to said second pixel electrode is largest or smallest, the potential provided to said common electrode is substantially equal to an average of the potentials provided to said first pixel electrode and to said second pixel electrode.

23. The liquid crystal display apparatus according to claim 1,

wherein said liquid crystal layer has a positive dielectric anisotropy.

24. The liquid crystal display apparatus according to claim 1, further comprising:

a scan driver;

a plurality of first scan lines connected to the scan driver;

a plurality of first signal lines connected to said first signal driver and disposed so as to cross said plurality of first scan lines; and

second signal lines connected to said second signal driver, and

wherein said plurality of pixels each correspond to an area enclosed by a corresponding one of said plurality of first scan lines and a corresponding

one of said plurality of first signal lines, said first pixel electrodes correspond to said first signal lines, and said second pixel electrodes corresponds to said second signal lines.

25. The liquid crystal display apparatus according to claim 24,

wherein said first substrate is provided with:

said first scan lines;

said first signal lines; and

first switch elements each arranged in a neighborhood of an intersection between said corresponding first scan line and first signal line, and

the second substrate is provided with second scan lines connected to said scan driver and arranged so as to cross said second signal lines, said second signal lines, and second switch elements each arranged in a neighborhood of an intersection between said corresponding said second scan line and said second signal line.

26. The liquid crystal display apparatus according to claim 24,

wherein said first substrate is provided with:

said first scan lines;

said first signal lines;

said second signal lines;

first switch elements each arranged in a

neighborhood of an intersection between said corresponding first scan line and said first signal line; and

second switch elements each arranged in a neighborhood of an intersection between said corresponding first scan line and said second signal line.

27. A liquid crystal display apparatus comprising:

a first substrate;

a second substrate arranged opposite said first substrate;

a liquid crystal layer sandwiched between said first substrate and said second substrate; and

a plurality of pixels which are sandwiched between said first substrate and said second substrate and form a display section,

wherein each of said pixels has a first and a second pixel electrodes each corresponding to said pixel and disposed on said first substrate, and a common electrode corresponding to said first and said second pixel elements and disposed on either said first or said second substrate.